

ABSTRACT:

A front end and a high frequency receiver (1) provided therewith are described, which front end comprises a quadrature low noise amplifier (2-1, 2-2) as a low noise amplifier. A high isolation between local oscillators (6-1, 6-2) and quadrature mixers (3-1, 3-2) is achieved thereby, reducing a DC offset at mixer outputs (7, 8). The quadrature
5 low noise amplifier may be implemented as a differential class AB cascode arrangement of MOST or FET semiconductors (15). A low distortion receiver (1) having a high linearity is the result.

Fig. 1

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